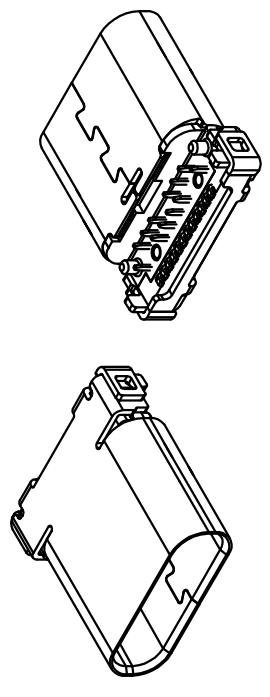
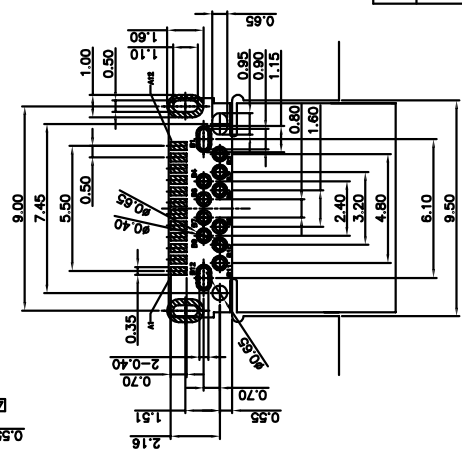
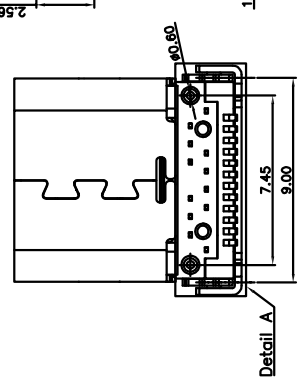
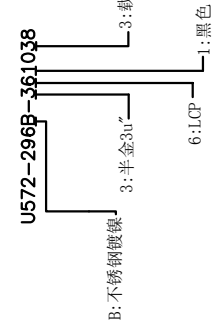


USE THE C FULL-PATTERNED RECEPTIVE INTERFACE PIN ASSIGNMENTS

Pin No.	Symbol	Description	Pin No.	Symbol	Description
A1	00D	Ground return	B12	00D	Ground return
A2	SST02	Positive half of first SuperSpeed TX differential pair	B11	SST02	Positive half of first SuperSpeed TX differential pair
A3	SST01	Negative half of first SuperSpeed TX differential pair	B10	SST01	Negative half of first SuperSpeed TX differential pair
A4	00S	Not used	B9	00S	Not used
A5	00C	Control plane	B8	SST2	Strobebed Line (SR)
A6	B01	USB 2.0 differential pair position 1	B7	B02	USB 2.0 differential pair position 2
A7	B01	USB 2.0 differential pair position 1	B6	B02	USB 2.0 differential pair position 2
A8	SST1	Strobebed Line (SR)	B5	012	Configuration
A9	00S	Not used	B4	00S	Not used
A10	SST02	Positive half of second SuperSpeed TX differential pair	B3	SST02	Positive half of second SuperSpeed TX differential pair
A11	SST01	Negative half of second SuperSpeed TX differential pair	B2	SST01	Negative half of second SuperSpeed TX differential pair
A12	00D	Ground return	B1	00D	Ground return



NOTES:
1.MATERIAL:
 1.1 Housing:thermoplastic plastics
 1.2 terminals:Copper Alloy
 Gold plated in contact area:
 Tin plated in termination
 Nickel Plated overall
 1.3 Shell:Steel&Copper Alloy
 Nickel/Au Plated overall
2.characteristics:
 2.1 Rating Voltage : 30V AC.
 2.2 Rating Current:
 5A for Vbus PIN;1.25A for Vconn PIN
 2.3 Contact Resistance:40 mΩ MAX.
 2.4 Insulation Resistance:100 MΩ MIN.
 2.5 Withstanding Voltage:AC 100V
 2.6 Mating force: 5~20N MAX
 2.7 Extraction force:8~20N Min
 2.8 Life test:10000Cycles MIN.
 2.9 Temperature Range: -30℃~+80℃.
 The product must be compliance ROHS



TOLERANCE UNLESS OTHERWISE SPECIFIED	
.XXX	±0.10
.XX	±0.20
.X	±0.30
.XX	±0.2
.XX	±0.2

APPROVED	
CHECKED	
DRAWN	Qinyuhua
DATE	2021.10.06

PART NAME:	TYPE C 24PIN 沉板端子前插后贴
PART No:	U572-296B-361038
PROJECTION:	UNIT: mm SCALE 1:1 SHEET 10F1 REV. A

FLW 深圳市华联威电子科技有限公司
 HUA LIAN WEI TECHNOLOGY ELECTRONICS CO;LTD.

RECOMMEND P.C.B LAY OUT (COMPONENT SIDE)
 TOLERANCE FOR PCB LAYOUT IS ±0.05
 KEEP OUT AREA